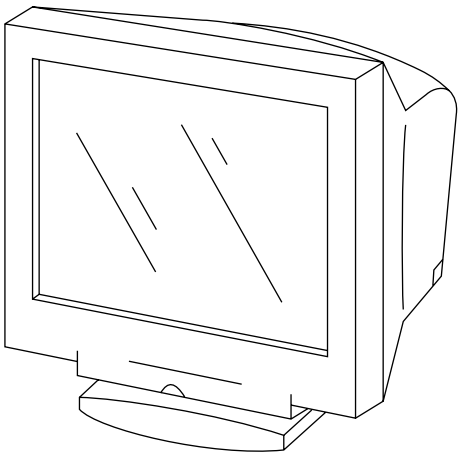
GDM-5410

SERVICE MANUAL

Japan Model
N.Hemisphere Model
S.Hemisphere Model

Chassis No. SCC-L22L-A



G1 CHASSIS

SPECIFICATIONS

CRT	0.24 mm aperture grille pitch 21 inches measured diagonally 90-degree deflection FD Trinitron
Viewable image size	Approx. 403.8 × 302.2 mm (w/h) (15.9 × 11.9 inches) 19.8" viewing image
Recommended resolution	Horizontal: 1280 dots Vertical: 1024 lines
Standard image area	Approx. 388 × 291 mm (w/h) (15 1/4 × 11 1/2 inches) or Approx. 364 × 291 mm (w/h) (14 3/8 × 11 1/2 inches)
Deflection frequency*	Horizontal: 30 to 121 kHz Vertical: 48 to 160 Hz
AC input voltage/current	100 to 240 V, 50 – 60 Hz, 2.0 – 1.0 A
Power consumption	Approx. 145 W
Dimensions	Approx. 501 × 498 × 498 mm (w/h/d) (19 3/4 × 19 5/8 × 19 5/8 inches)
Mass	Approx. 32 kg (70 lb 9 oz)
Plug and Play	DDC1/2B/2Bi, GTF**

* Recommended horizontal and vertical timing condition
- Horizontal sync width duty should be more than 4.8% of total horizontal time or 0.8 µs, whichever is larger.
- Horizontal blanking width should be more than 2.3 µsec.
- Vertical blanking width should be more than 450 µsec.

** If the input signal is Generalized Timing Formula (GTF) compliant, the GTF feature of the monitor will automatically provide an optimal image for the screen.

Design and specifications are subject to change without notice.

COLOR GRAPHIC DISPLAY

Sun microsystems

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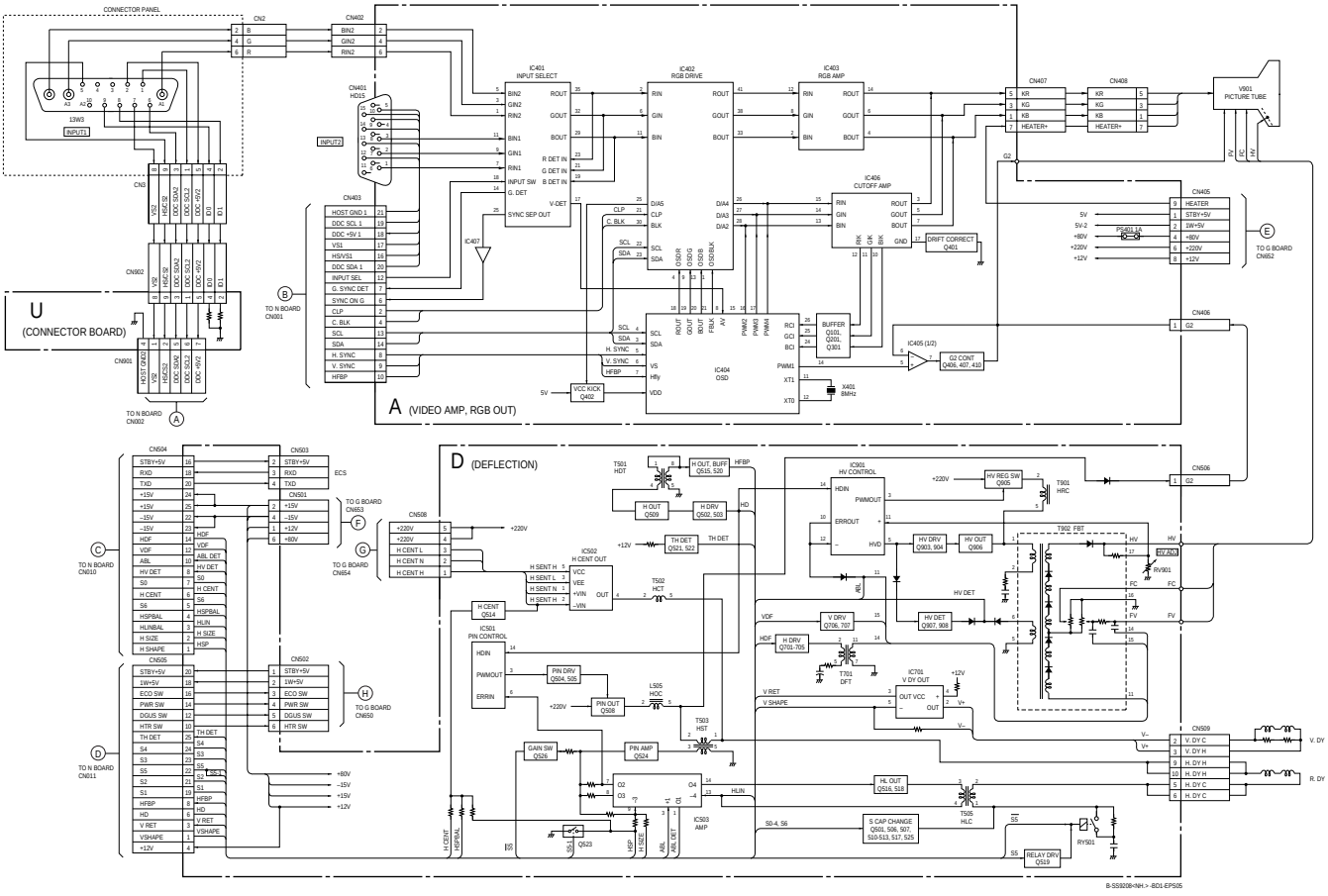
SECTION 5
DIAGRAMS

5-1. BLOCK DIAGRAMS



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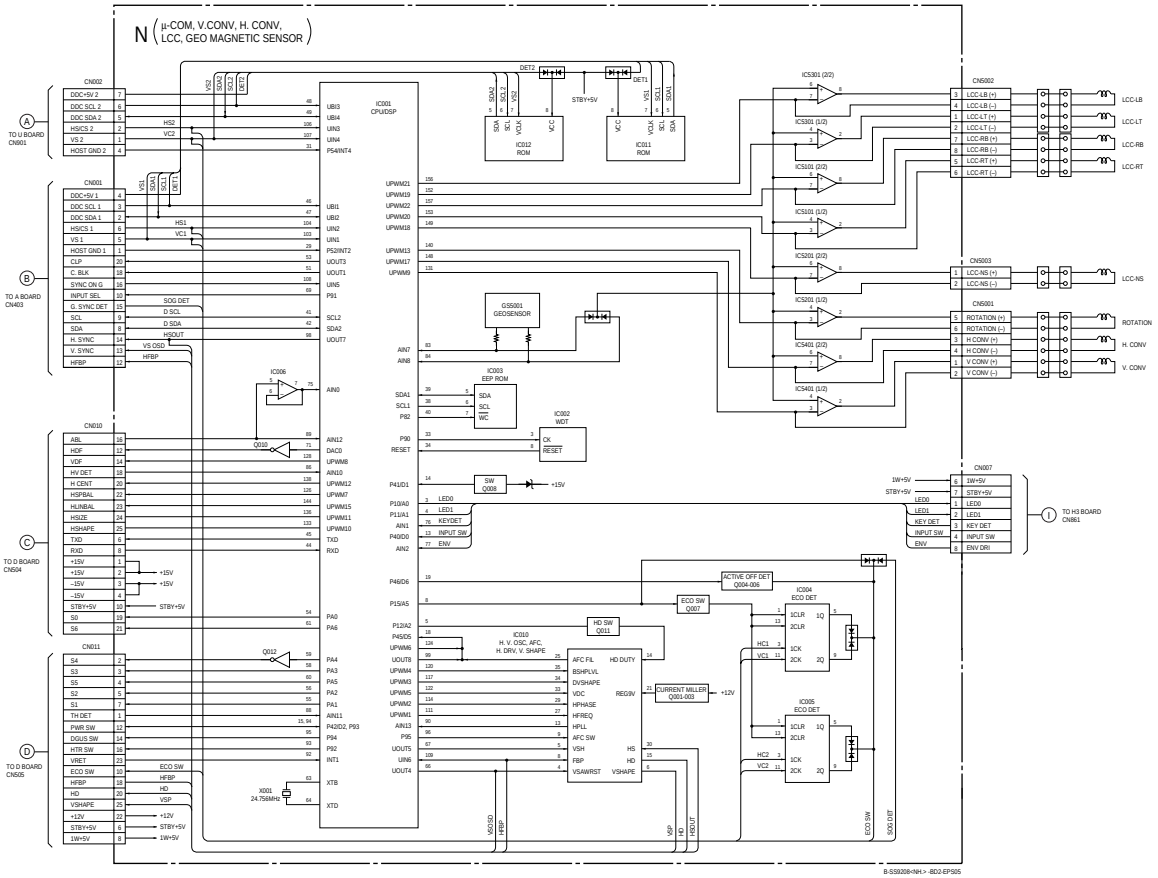

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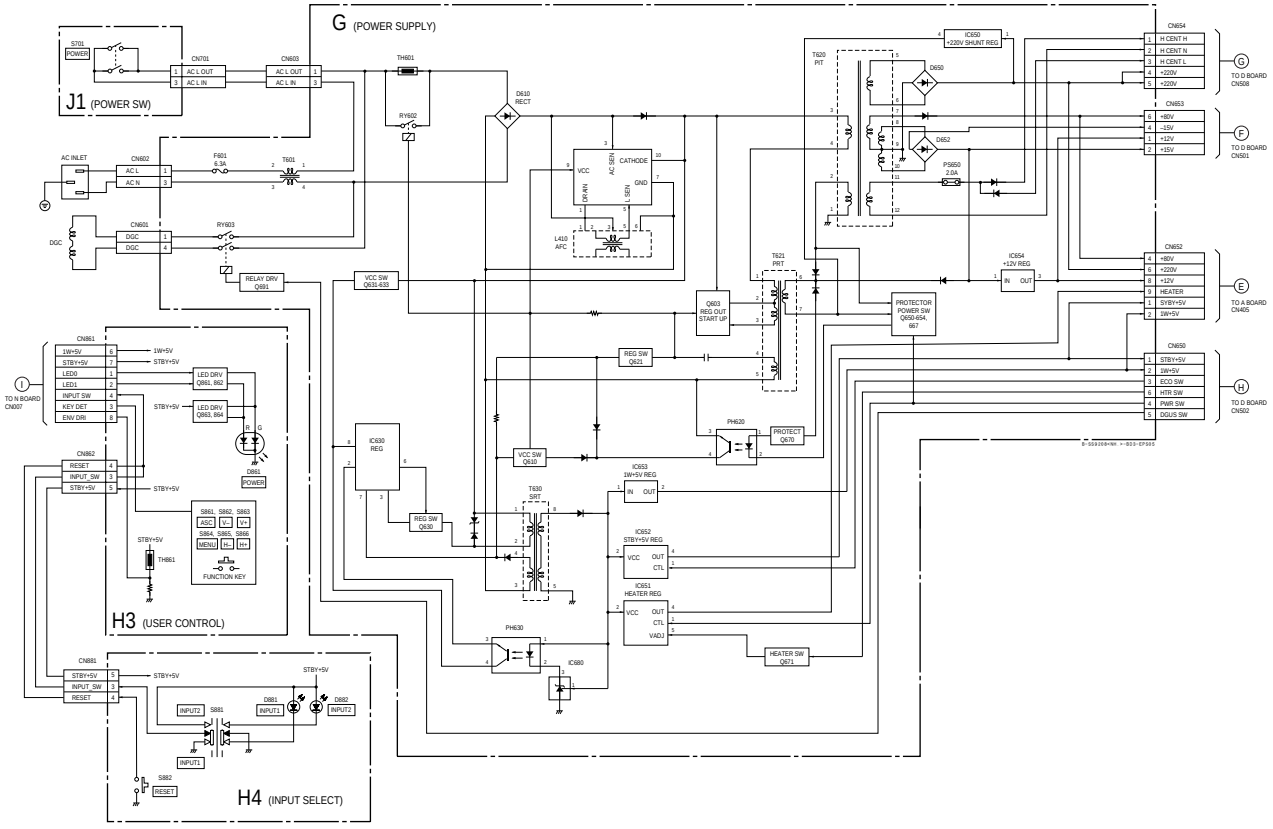

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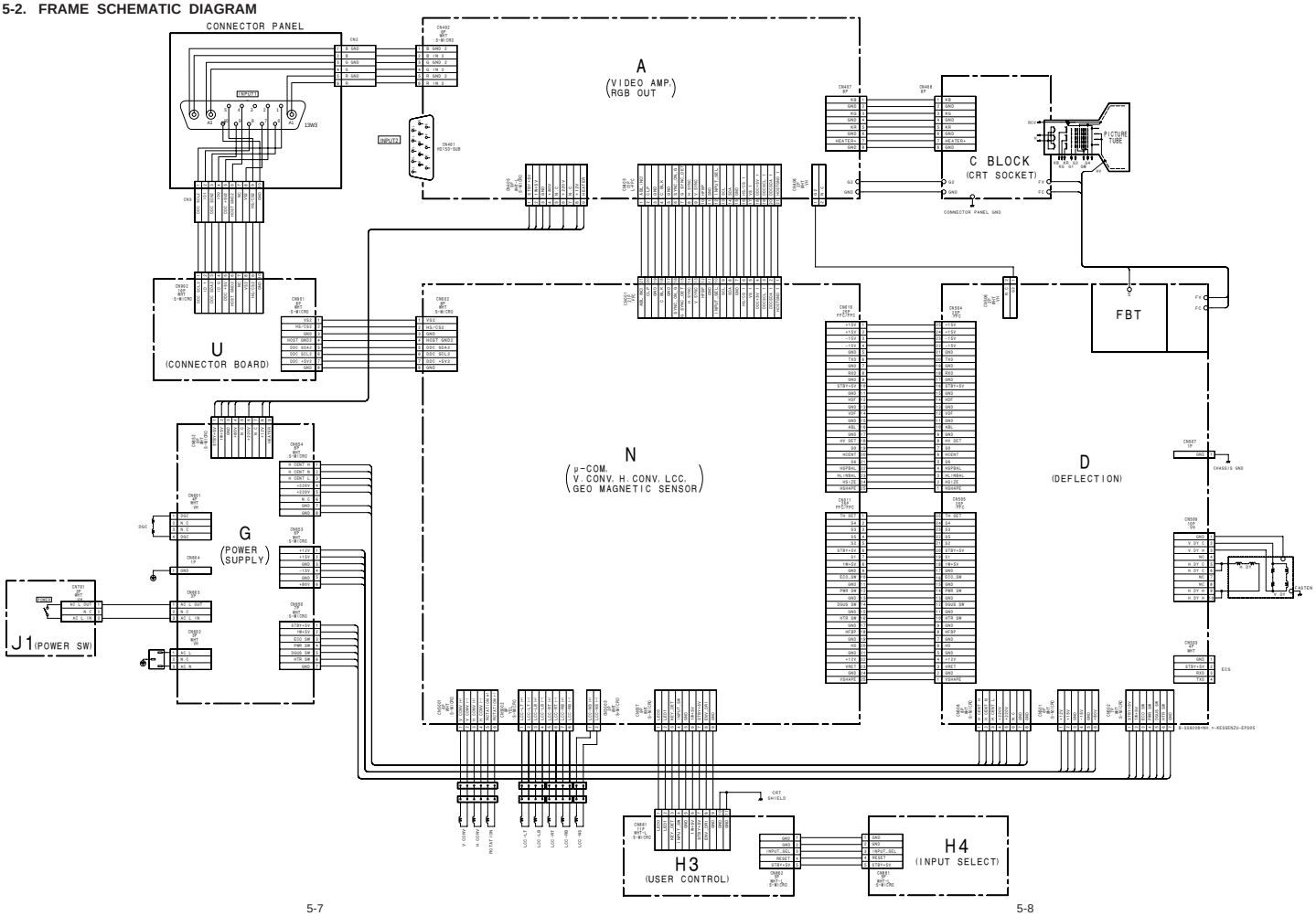
5-2. FRAME SCHEMATIC DIAGRAM



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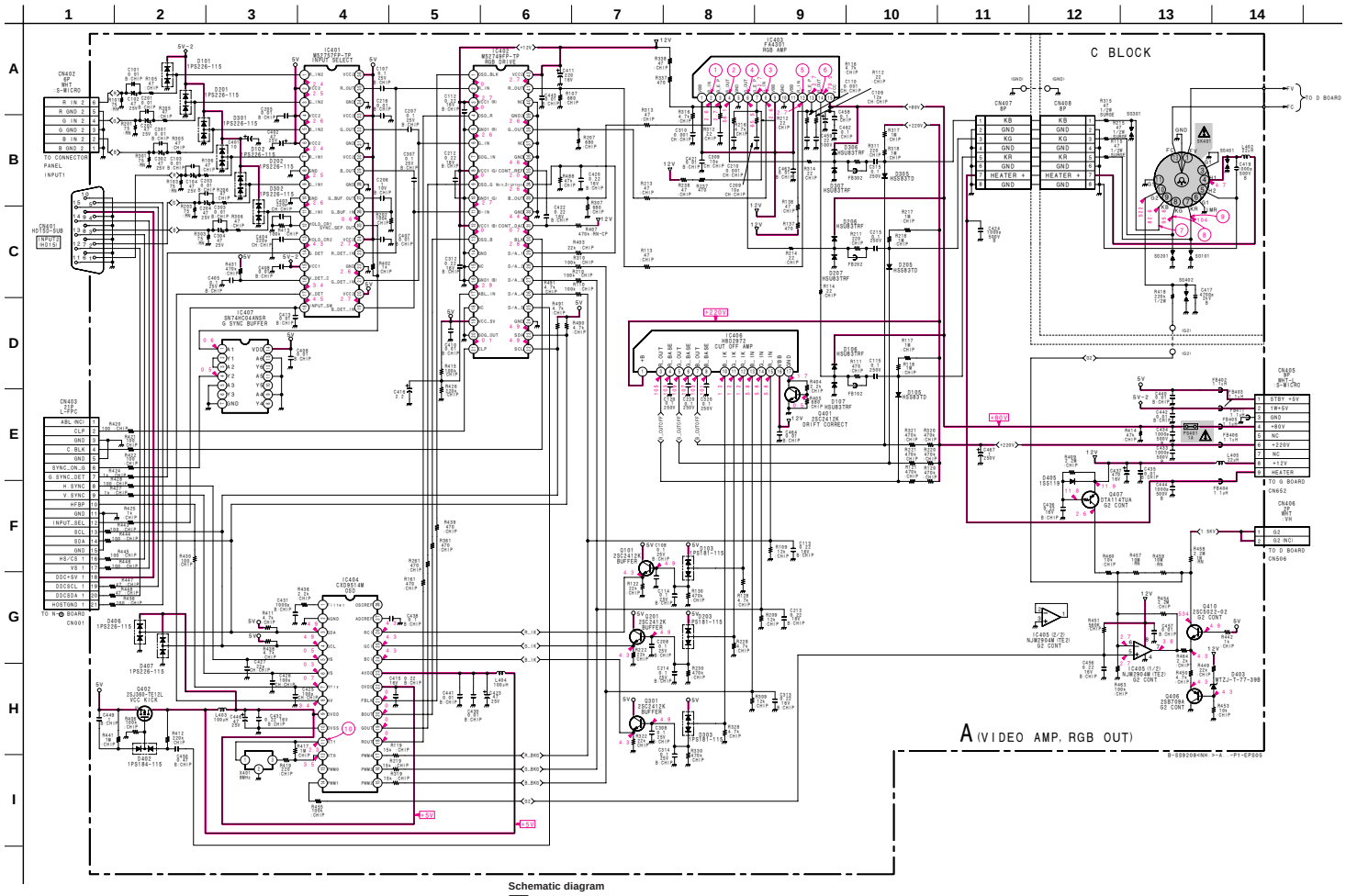
(1) Schematic Diagram of A Board



Schematic diagram
A board

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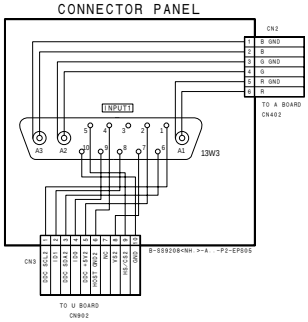
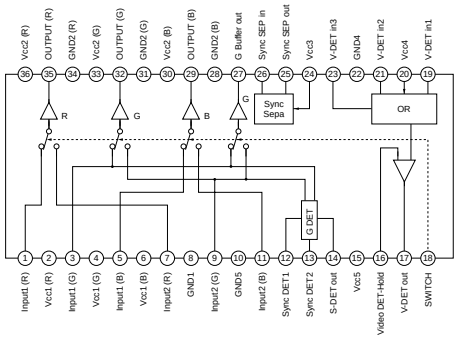


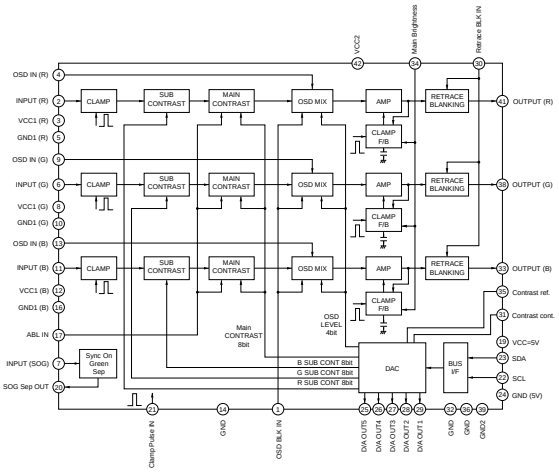
• **A BOARD WAVEFORMS**

① 4.7 Vp-p (H)	② 66.0 Vp-p (H)	③ 4.8 Vp-p (H)
④ 64.0 Vp-p (H)	⑤ 4.6 Vp-p (H)	⑥ 64.0 Vp-p (H)
⑦ 65.0 Vp-p (H)	⑧ 64.0 Vp-p (H)	⑨ 64.0 Vp-p (H)
⑩ 2.2 Vp-p (8MHz)		

• **A BOARD IC401 M52757FP**



• **A BOARD IC402 M52749FP**



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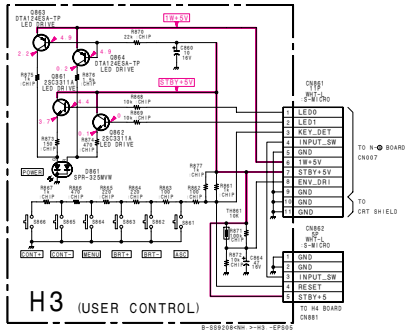
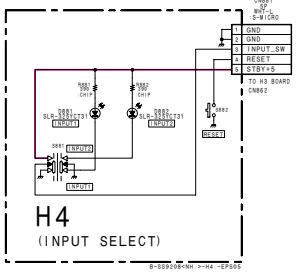
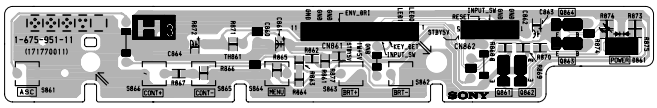
(2) Schematic Diagrams of H3, H4 Boards

H3 [USER CONTROL] H4 [INPUT SELECT]

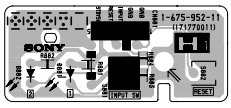




— H3 BOARD —



— H4 BOARD —



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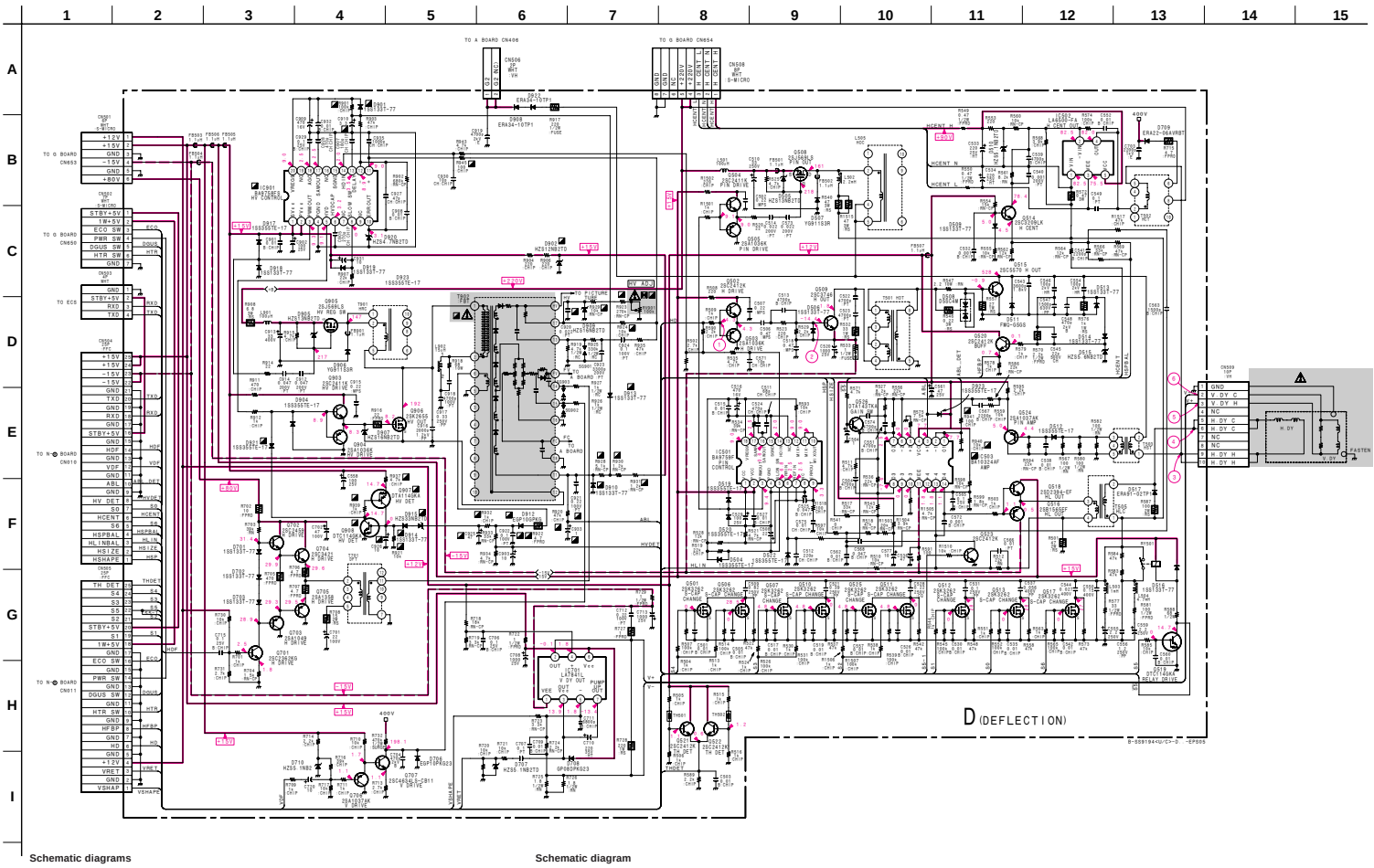
(3) Schematic Diagram of D Board



Schematic diagrams
H3 H4 boards

Schematic diagram
D board

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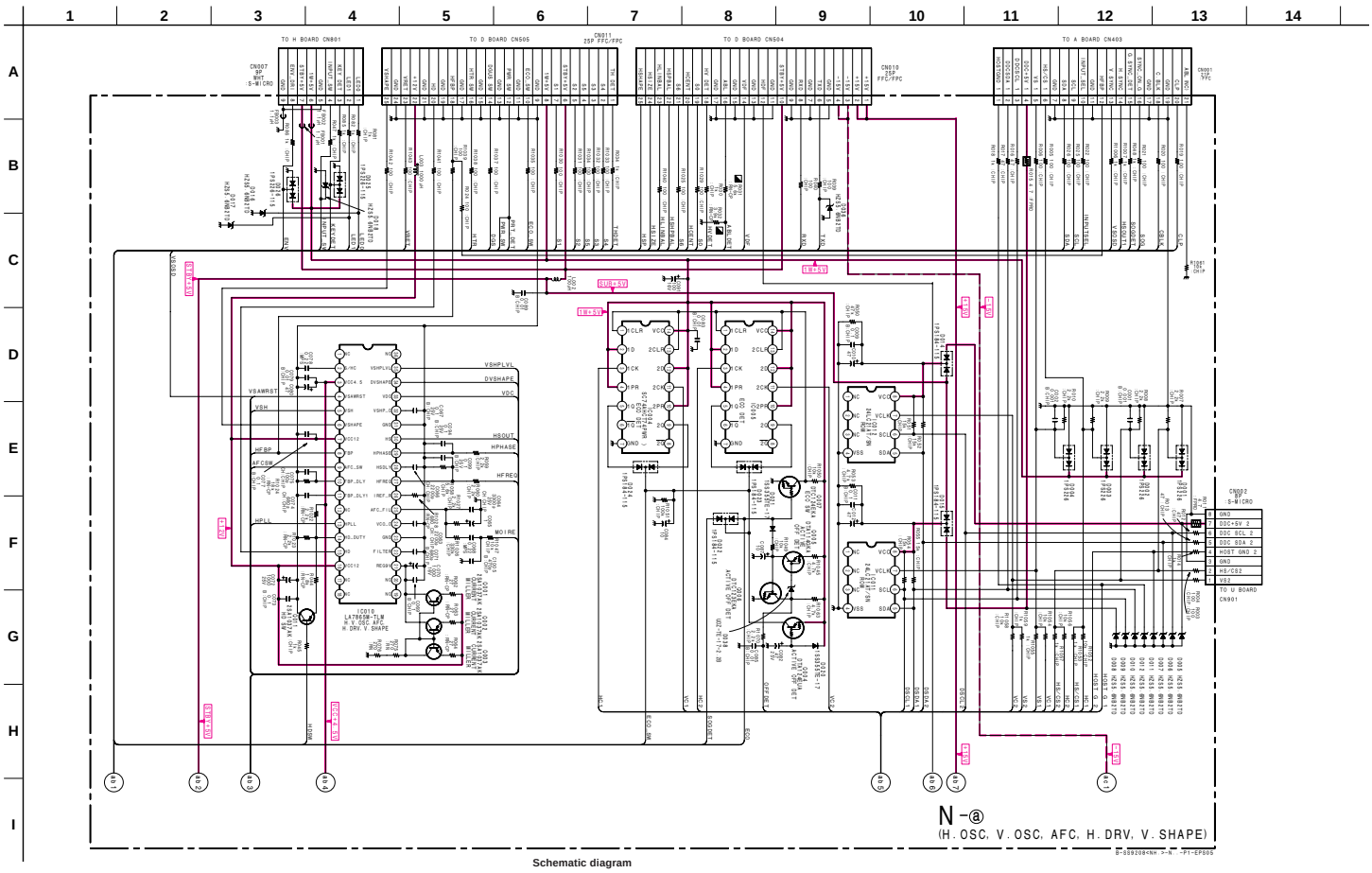
(4) Schematic Diagrams of N (ⓐ, ⓑ, ⓒ) Board

- Divided circuit diagram
  One sheet of N board circuit diagram is divided into three sheets, each having the code N-ⓐ to N-ⓒ. For example, the destination ab1 on the code N-ⓐ sheet is connected to ab1 on the N-ⓑ sheet.

a b 1
- Ref. No.
- Circuit diagram division code



Schematic diagram
N-ⓐ board ➡

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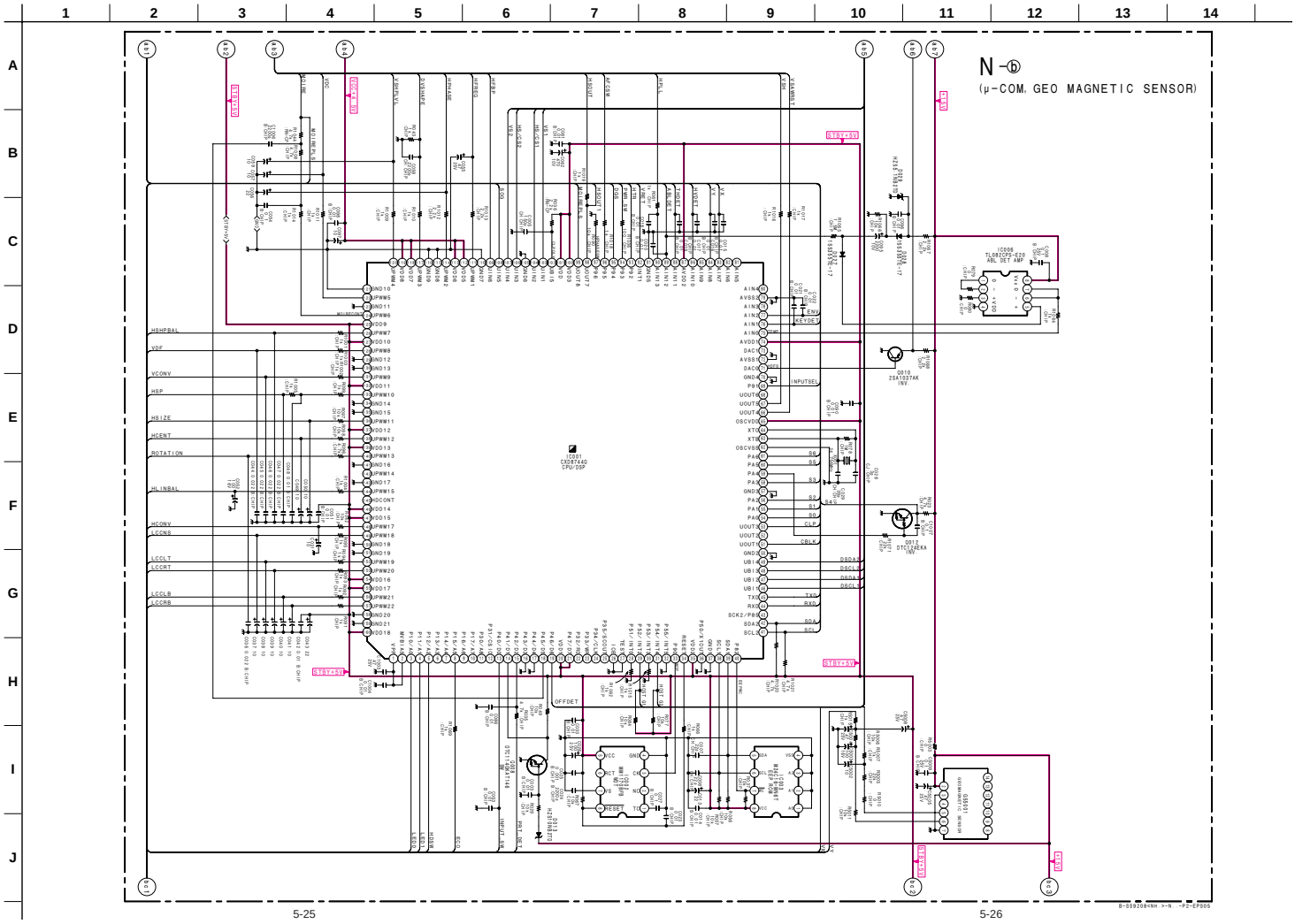

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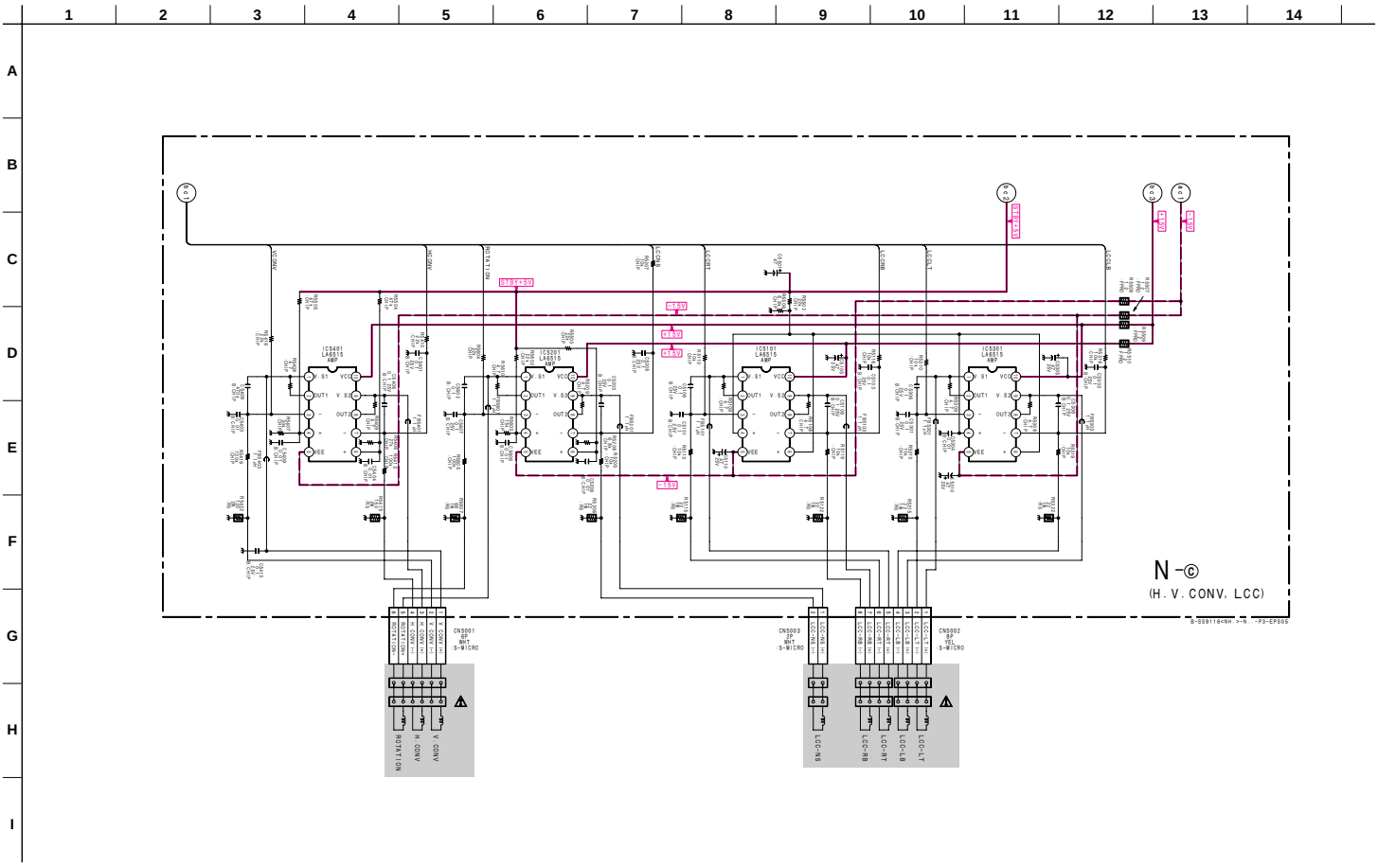

Schematic diagram
◀ N -ⓑ board

Schematic diagram
N -ⓒ board ▶

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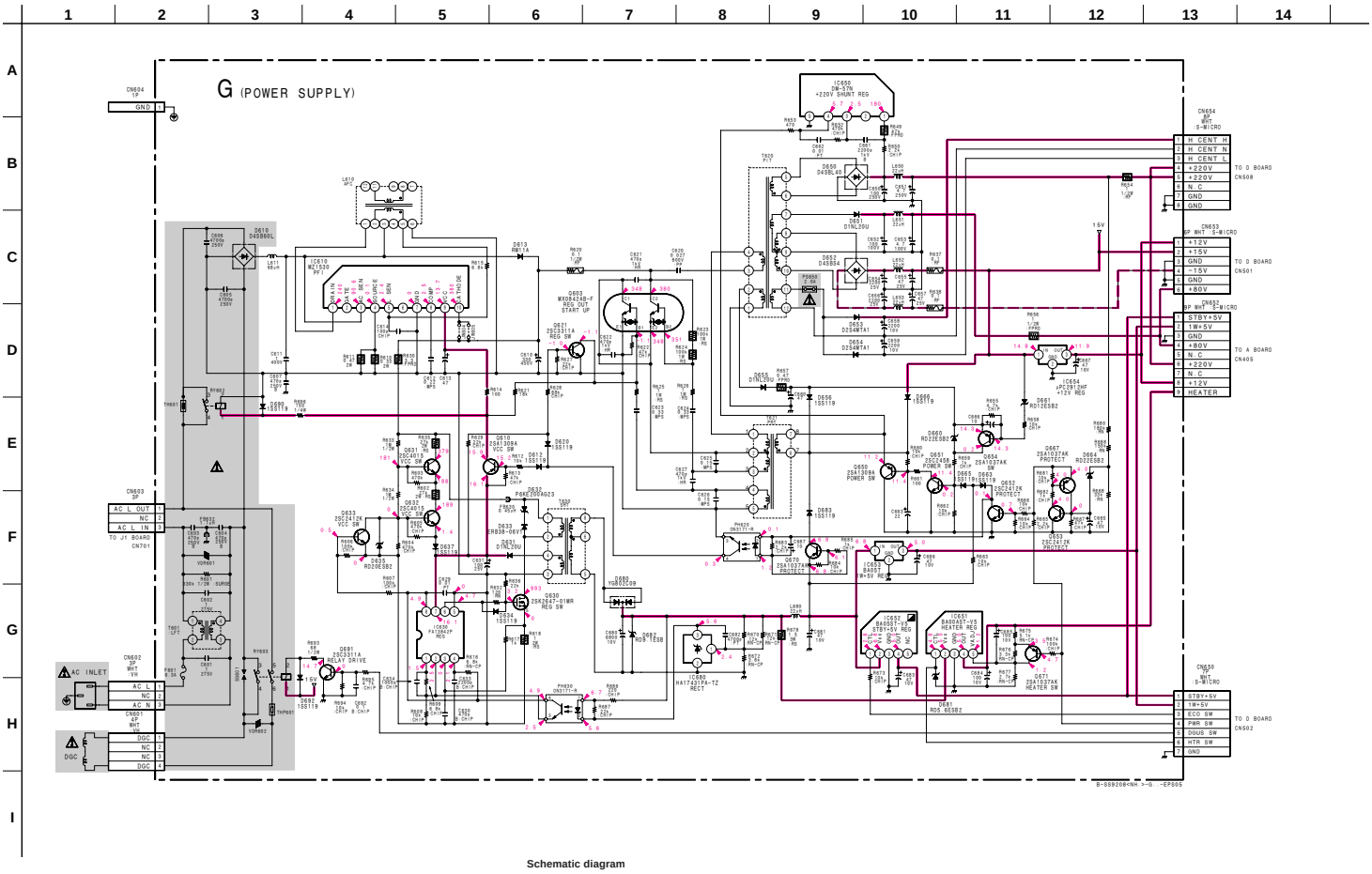
(5) Schematic Diagram of G Board



Schematic diagram

G board ➡

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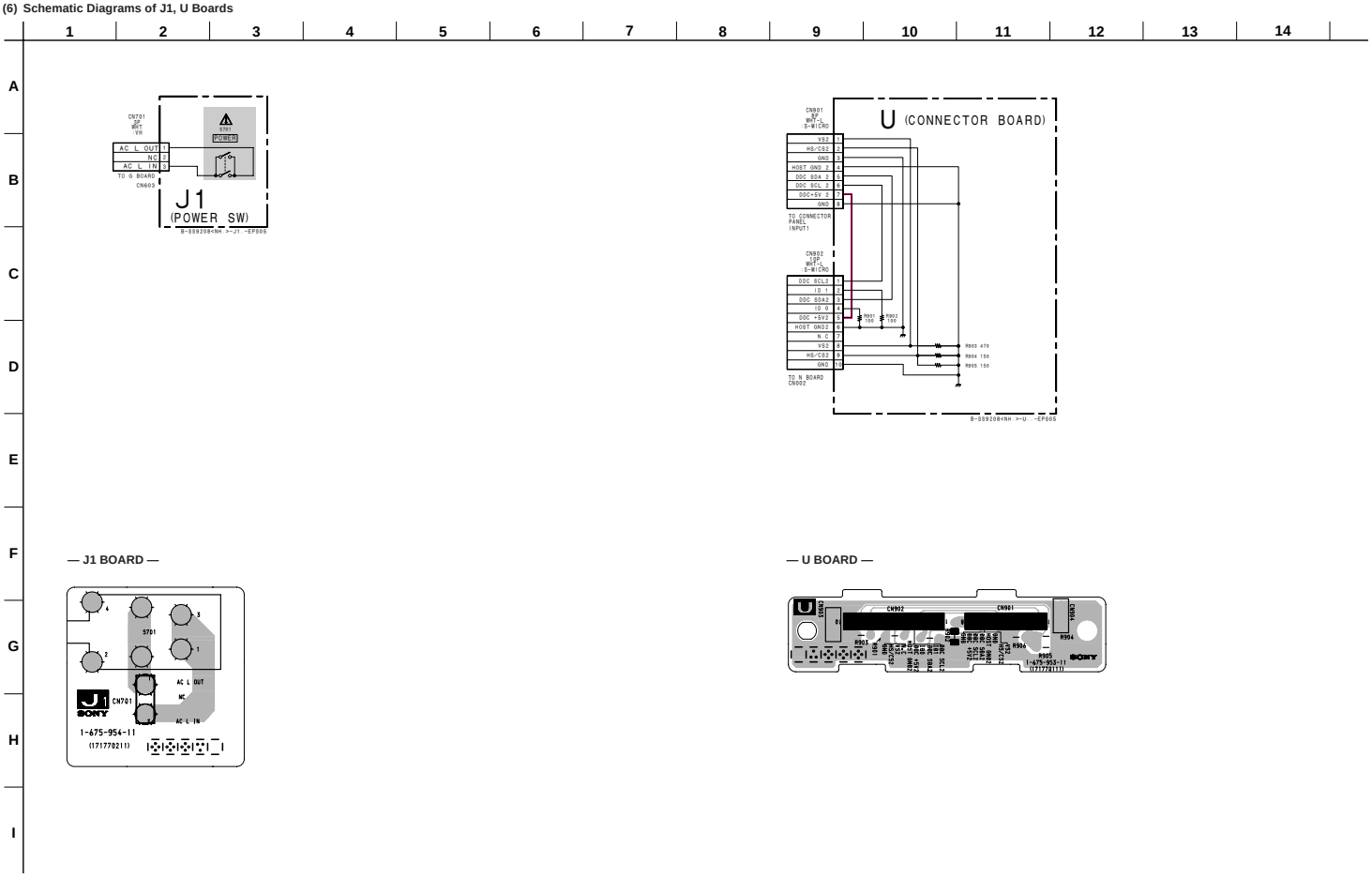
J1 [POWER SW] U [CONNECTOR BOARD]

(6) Schematic Diagrams of J1, U Boards



— J1 BOARD —

— U BOARD —

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